FACULTY PROFILE

Name of the faculty: P.NISHMIKA

Department: ECE
College: S R Gudlavalleru Engineering College
E-Mail: Pothana.nishmika@gmail.com

RESIDENTIAL ADDRESS: D/No:42-65-10, Block No:65, Ajith Singh Nagar, Vijayawada-15.

ACADEMIC QUALIFICATIONS: M.Tech.

ACADEMIC REWARDS / ACHIEVEMENTS:

➢ M.Tech (VLSI & Embedded Systems) with an aggregate of 82% from V R Siddhartha Engineering College, JNTUK University in 2021-2023.
➢ B.Tech (E.C.E) with an aggregate of 69% from V R Siddhartha Engineering College, JNTUK University in 2017-2021.
➢ Intermediate with an aggregate of 92% from State Board of Intermediate in 2015-2017.
➢ SSC with an aggregate of 70% Indian Council of Secondary Education in 2014-2015.

PROFESSIONAL EXPERIENCE:

Designation	Institution	Period
Assistant Professor	Gudlavalleru Engineering College	June 21st 2024 to till date

INTERNSHIP/IN-PLANT TRAINING DETAILS:

Industry: SoCtronics Technologies Pvt. Ltd., Guntur, Andhra Pradesh.
Stream: Design Verification.
Duration: October 10,2022 – May 10,2023.

Industry: NPHSAT SYSTEMS Pvt. Ltd., Vijayawada, Andhra Pradesh.
Stream: Small satellite design.
Duration: March 24,2020 – April 25,2020.

PROFESSIONAL SKILLS:

➢ Strong knowledge of Verilog HDL, System Verilog, UVM, C/C++, Python
➢ Synopsys – VCS, Mentor Graphics- Questasim, VC Spyglass Lint
➢ RTL Coding using Synthesizable constructs of Verilog, FSM based design, Simulation, CMOS Fundamentals, Code Coverage, Functional Coverage, Synthesis.

CERTIFICATE COURSE:

➢ Advanced VLSI Design and Verification – Maven Silicon
➢ Digital Circuits - NPTEL

SPECIALIZATION: VLSI DESIGN

RESEARCH PAPERS / BOOKS: ------

PAPERS PUBLISHED IN IEEE CONFERENCE:

Nishmika P and V H Prasad Reddy –"VERIFICATION OF COVERAGE DRIVEN AND ASSERTION-BASED AXI4 PROTOCOL VIP USING UVM", Published in: 2023 First International Conference on Cyber Physical Systems, Power Electronics and Electric Vehicles (ICPEEV), **ISBN:**979-8-3503-2357-3,
DOI: 10.1109/ICPEEV58650.2023.10391880

CERTIFICATIONS/Training Programs/FDPs Attended No:

1. One week Online Faculty Development Program on "**Semiconductor Devices, Modelling and Circuit Technology**" conducted by Lendi Institute of Engineering and Technology, Vizianagaram from 19-08-2024 to 24-08-2024.

2. Three-day Online Faculty Development Program on "**Generative AI**" organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26 JUN 2024 to 28 JUN 2024.

3. Training in Advanced VLSI Design and Verification from Maven Silicon, Bangalore, from September 2023 to April 2024

LIST OF WORKSHOPS /CONFERENCES /SEMINARS ATTENDED:

- Attended a workshop on "Circuits and Systems(CAS) research forum", organized by IEEE CAS/EDS, Vijayawada.

- Attended an International Conference on Cyber Physical Systems, Power Electronics and Electric Vehicles (ICPEEV) at Mahindra University, Hyderabad, India.

PAPER PRESENTATION IN SEMINARS:

- Presented a paper on –"VERIFICATION OF COVERAGE DRIVEN AND ASSERTION-BASED AXI4 PROTOCOL VIP USING UVM" at Mahindra University, Hyderabad.

PROFESSIONAL BODIES MEMBERSHIPS:

- Member in "**International Association of Engineers (IAENG)**".

GUEST LECTURES DELIVERED: ----

CONSULTANCY ACTIVITIES: ----

GRANTS FETCHED: ----

SUBJECTS Handled:

Theory:

1. Dl&CO
2. Automotive Electronics(Open elective)

Labs:

1. Electrical and Electronics Workshop(EEW)
2. IOT Lab
3. RFOC Lab
4.

Extra Curricular Activities: Department level activities

Declaration

      I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date: 30-12-2024   Nishmika.Pothana
Place Gudlavalleru   Signature